

図2

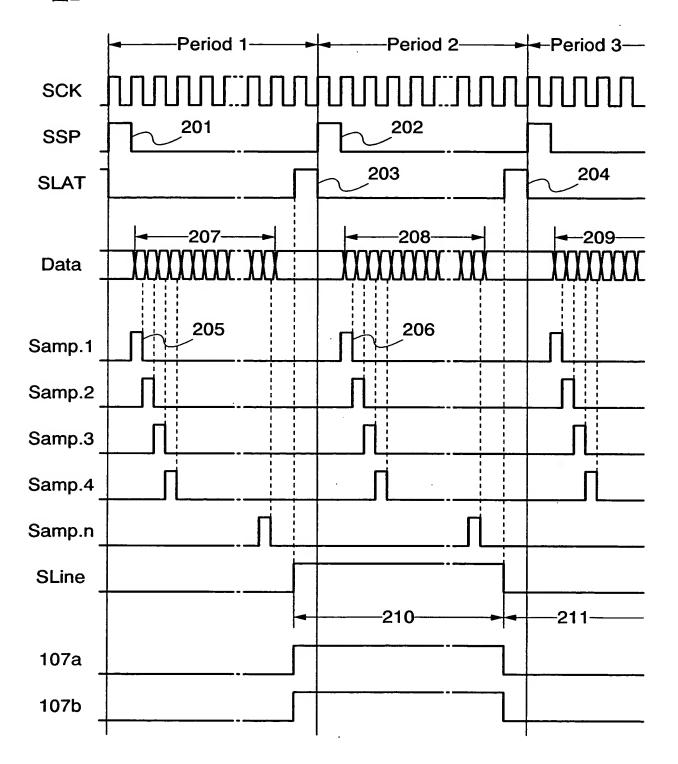


図3A

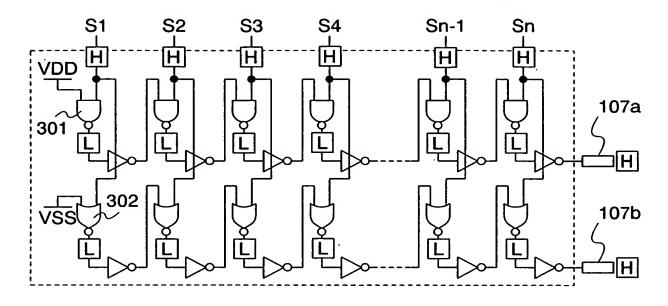
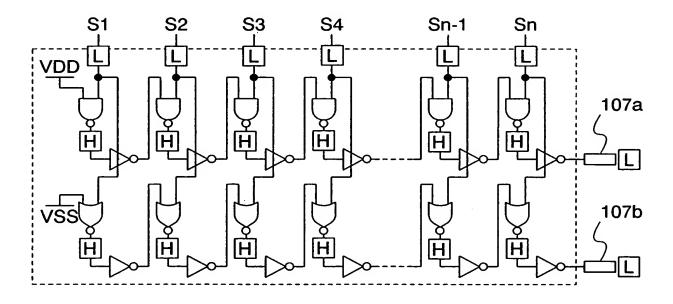
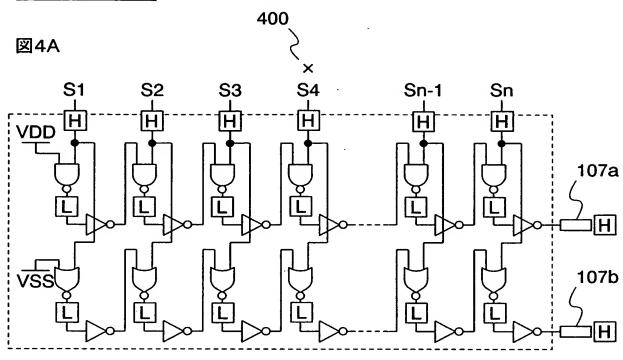
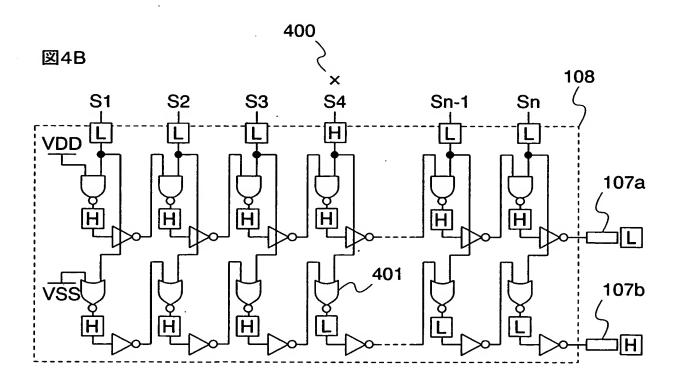


図3B

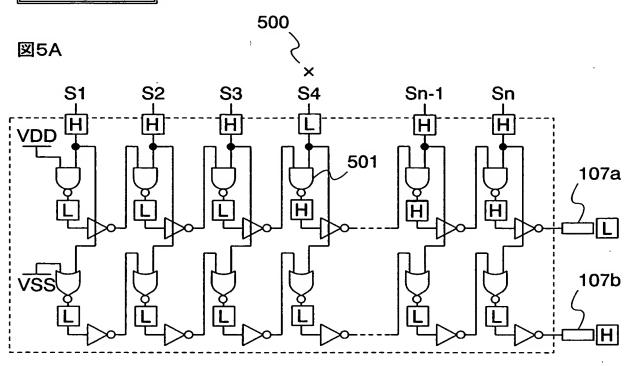


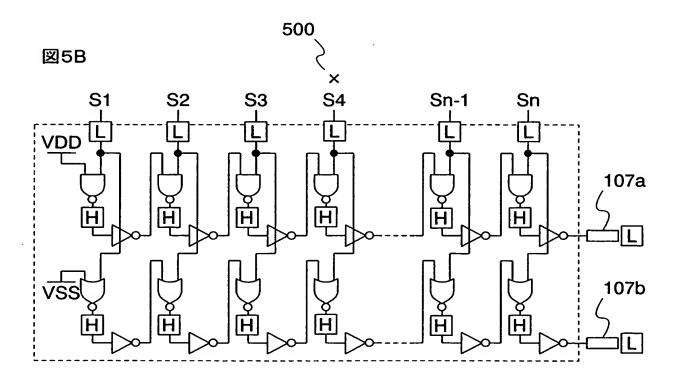
動作不良モードA



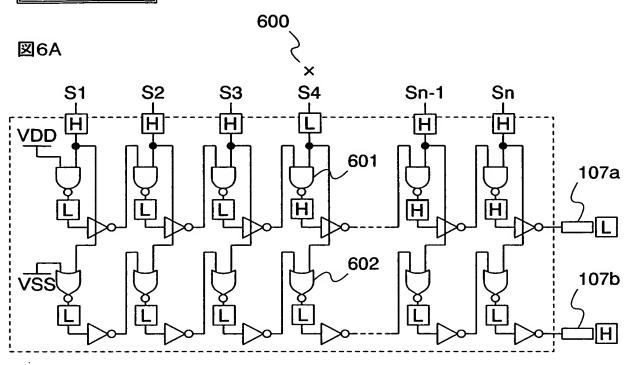


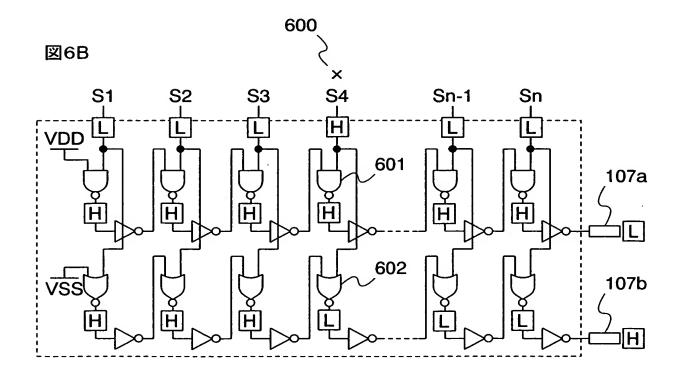
動作不良モードB

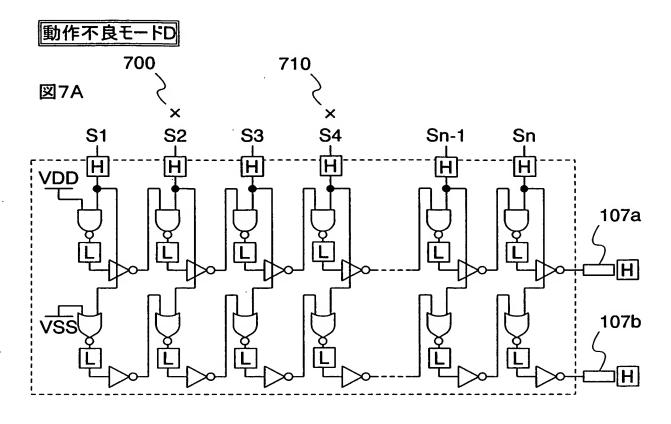


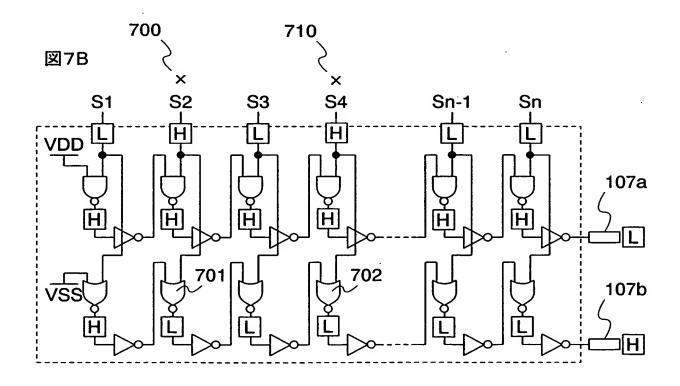


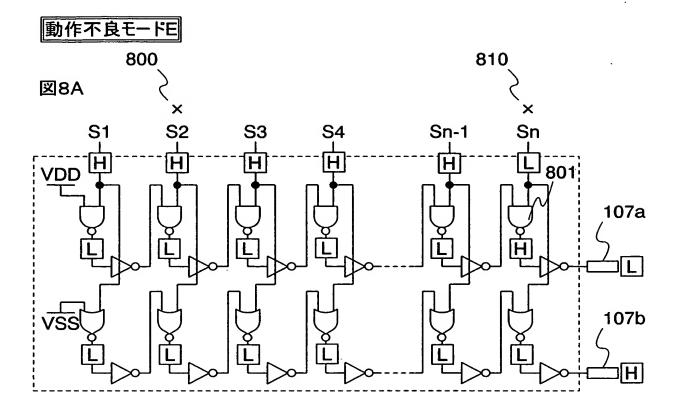
動作不良モードロ

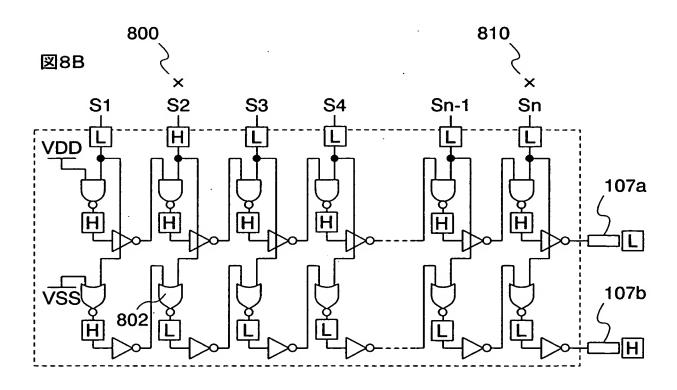


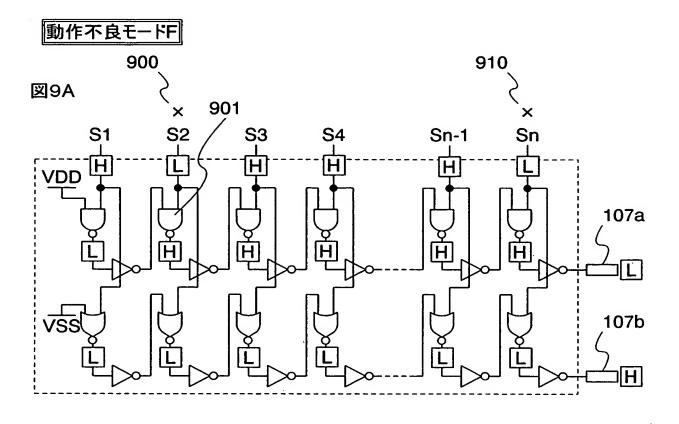












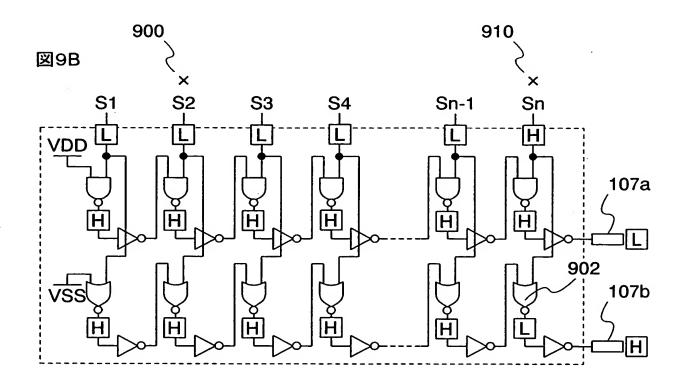


図10A

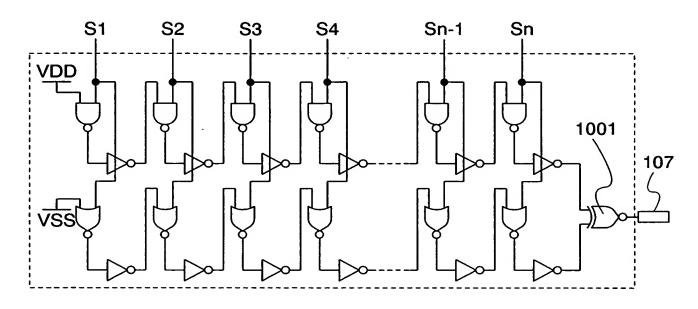
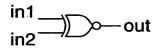


図10



in1	in2	out
Н	Н	H
Η	L	L
L	Η	L
L	L	Н

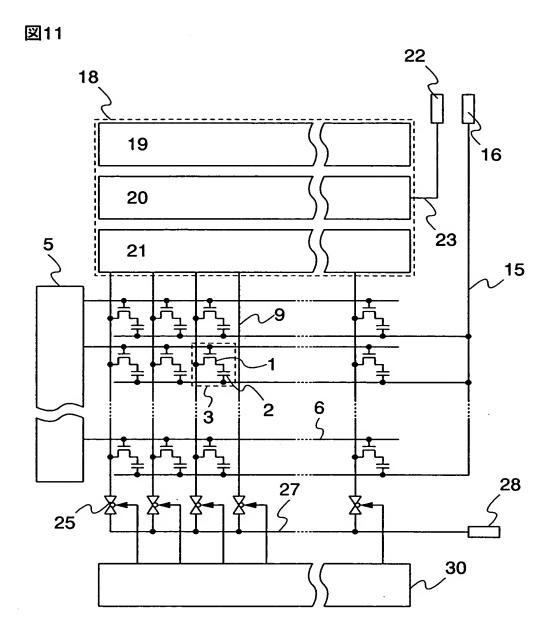
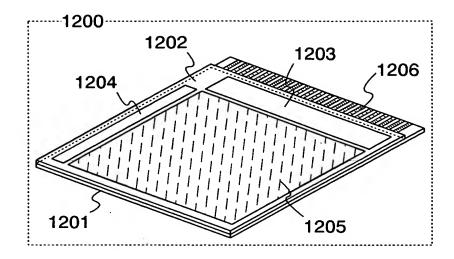


図12A



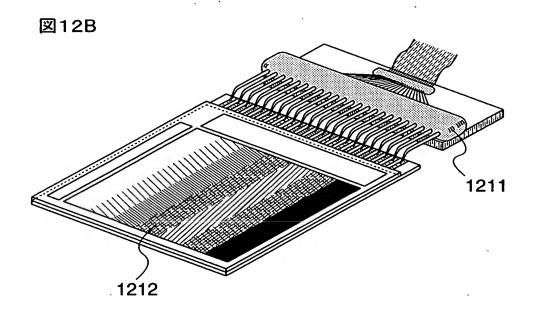


図13

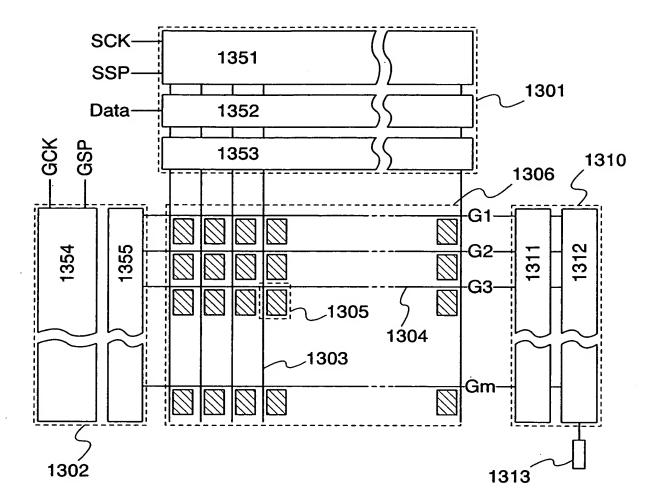


図14

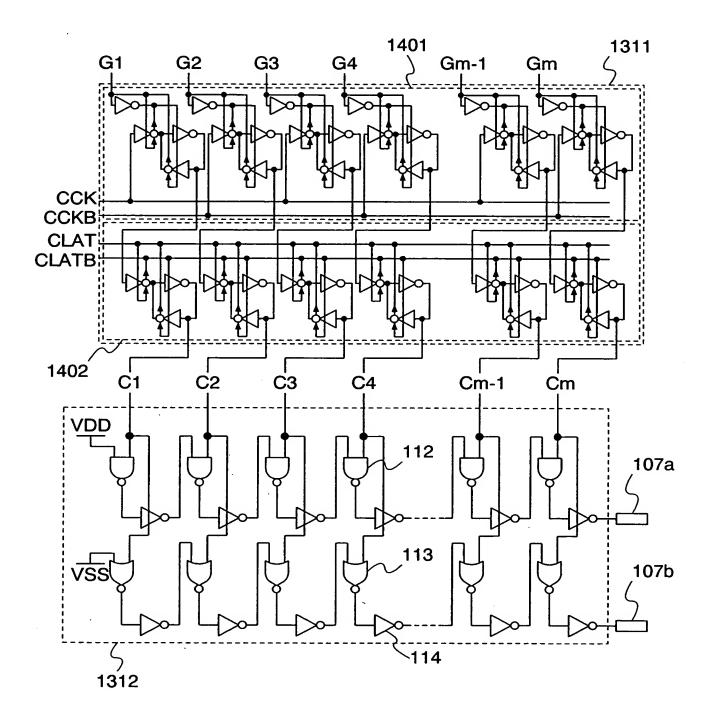


図15

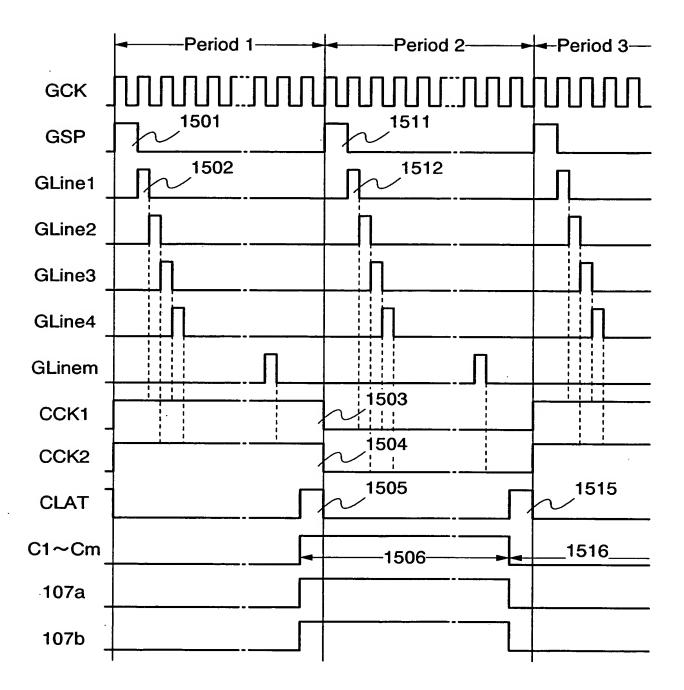


図16

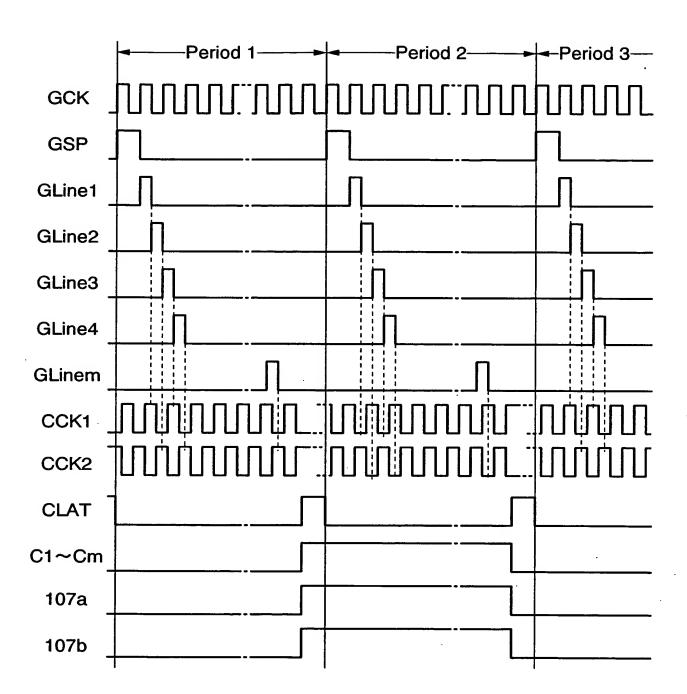


図17

